IEEE Xplore RELEASE 2.0

Welcome United States Patent and Trademark Office

Search Session History

BROWSE   SEARCH   IEEE XPLORE GUIDE

Mon, 13 Jun 2005, 12:40:48 AM EST

Edit an existing query or compose a new query in the Search Query Display.

Search Query Display

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

#1   ((static schedule and trace and instruction?)<in>metadata)

#2   ((thekkath and static schedule)<in>metadata)

#3   ((static schedule)<in>metadata)

#4   ((static schedule and trace)<in>metadata)

#5   ((static schedule and trace)<in>metadata)

#6   ((static schedule and instruction and pipeline?)<in>metadata)

#7   ((static schedule and instruction and pipeline?)<in>metadata)

#8   ((static schedule and instruction and pipeline?)<in>metadata)

#9   ((static schedule and trace)<in>metadata)

Help   Contact Us   Privacy & !

© Copyright 2005 IEEE --

Indexed by

Inspec

IEEE Xplore
RELEASE 2.0

Welcome United States Patent and Trademark Office

Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE

Results for "((static schedule and instruction and pipeline?)<in>metadata)"

☑e-mail

Your search matched **1** of **1168854** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» <u>View Session History</u>

» <u>New Search</u>

» **Key**

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

| ((static schedule and instruction and pipeline?)<in>metadata) | » |

☐ Check to search only within this results set

Display Format: ◉ Citation   ○ Citation & Abstract

☐   1. **Dynamic instruction scheduling and the Astronautics ZS-1**
       Smith, J.E.;
       Computer
       Volume 22, Issue 7, July 1989 Page(s):21 - 35

       <u>AbstractPlus</u> | Full Text: <u>PDF</u>(1168 KB)   IEEE JNL

Help   Contact Us   Privacy & :

© Copyright 2005 IEEE –

IEEE Xplore
RELEASE 2.0

Welcome United States Patent and Trademark Office

Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE

Results for "((static schedule and trace)<in>metadata)"
Your search matched **1** of **1168854** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» View Session History

» New Search

» **Key**

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

[((static schedule and trace)<in>metadata)] [»]

☐ Check to search only within this results set

Display Format: ◉ Citation ○ Citation & Abstract

☐ 1. **Load distribution via static scheduling and client redirection for replicated Web s**
Wenting Tang; Mutka, M.W.;
Parallel Processing, 2000. Proceedings. 2000 International Workshops on
21-24 Aug. 2000 Page(s):127 - 133

AbstractPlus | Full Text: PDF(608 KB) IEEE CNF

Help   Contact Us   Privacy & :

Indexed by
Inspec

Dial g DataStar

options   logoff   feedback   help

databases   easy search

Advanced Search: INSPEC - 1969 to date (INZZ)

limit

Search history:

No.	Database	Search term	Info added since	Results	
1	INZZ	static ADJ schedule AND trace	unrestricted	0	-

hide | delete all search steps... | delete individual search steps...

Enter your search term(s): Search tips

| | whole document   ⬛ |

Information added since: [   ] or: [none   ⬛]
(YYYYMMDD)

search

Select special search terms from the following list(s):
⬤ Classification codes A: Physics, 0-1
⬤ Classification codes A: Physics, 2-3
⬤ Classification codes A: Physics, 4-5
⬤ Classification codes A: Physics, 6
⬤ Classification codes A: Physics, 7
⬤ Classification codes A: Physics, 8
⬤ Classification codes A: Physics, 9
⬤ Classification codes B: Electrical & Electronics, 0-5
⬤ Classification codes B: Electrical & Electronics, 6-9
⬤ Classification codes C: Computer & Control
⬤ Classification codes D: Information Technology
⬤ Classification codes E: Manufacturing & Production
⬤ Treatment codes
⬤ INSPEC sub-file
⬤ Publication types
⬤ Language of publication

WEST Search History

| Hide Items | Restore | Clear | Cancel |

DATE: Monday, June 13, 2005

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ	
☐	L11	L8 and trace and instruction?	10
☐	L10	L8 and instruction pipeline?	0
☐	L9	L8 and (tracing near5 instruction?)	0
☐	L8	static schedule	53
☐	L7	L6 and static schedule	0
☐	L6	(tracing near5 instruction?) and instruction pipeline?	10
☐	L5	L1 and tracing	1
☐	L4	L1 and instruction pipeline?	0
☐	L3	L2 and tracing near instructions	0
☐	L2	L1 and trac$ and instruction?	17
☐	L1	thekkath.in.	25

END OF SEARCH HISTORY

Hit List

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs |

| Generate OACS |

Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1.   Document ID: US 5802585 A

L5: Entry 1 of 1   File: USPT   Sep 1, 1998

US-PAT-NO: 5802585
DOCUMENT-IDENTIFIER: US 5802585 A

TITLE: Batched checking of shared memory accesses

DATE-ISSUED: September 1, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Scales; Daniel J.	Palo Alto	CA		
Thekkath; Chandramohan A.	Palo Alto	CA		

US-CL-CURRENT: 711/154; 711/147, 717/158

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw. De |

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Term	Documents
TRACING	34057
TRACINGS	3039
(1 AND TRACING).PGPB,USPT.	1
(L1 AND TRACING).PGPB,USPT.	1

Display Format: | - | | Change Format |

Previous Page   Next Page   Go to Doc#

Hit List

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs |

| Generate OACS |

Search Results - Record(s) 1 through 10 of 10 returned.

☐ 1. Document ID: US 6106573 A

L6: Entry 1 of 10   File: USPT   Aug 22, 2000

US-PAT-NO: 6106573
DOCUMENT-IDENTIFIER: US 6106573 A
** See image for **Certificate of Correction** **

TITLE: Apparatus and method for tracing microprocessor instructions

DATE-ISSUED: August 22, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mahalingaiah; Rupaka	Austin	TX		
Pickett; James K.	Austin	TX		

US-CL-CURRENT: 717/128; 712/204, 712/23, 712/3

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | | Claims | KWIC | Draw De |

☐ 2. Document ID: US 5933626 A

L6: Entry 2 of 10   File: USPT   Aug 3, 1999

US-PAT-NO: 5933626
DOCUMENT-IDENTIFIER: US 5933626 A
** See image for **Certificate of Correction** **

TITLE: Apparatus and method for tracing microprocessor instructions

DATE-ISSUED: August 3, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mahalingaiah; Rupaka	Austin	TX		
Pickett; James K.	Austin	TX		

US-CL-CURRENT: 712/227; 714/38

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | | Claims | KWIC | Draw De |

☐ 3. Document ID: US 5922070 A

L6: Entry 3 of 10   File: USPT   Jul 13, 1999

US-PAT-NO: 5922070
DOCUMENT-IDENTIFIER: US 5922070 A

TITLE: Pipelined data processing including program counter recycling

DATE-ISSUED: July 13, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Swoboda; Gary L.	Sugar Land	TX		
Hammes; Mark R.	Houston	TX		
Deao; Douglas	Brookshire	TX		
Balmer; Keith	Bedford			GB
Ing-Simmons; Nick	Huntingdon			GB

US-CL-CURRENT: 712/244; 712/227

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 4. Document ID: US 5724566 A

L6: Entry 4 of 10   File: USPT   Mar 3, 1998

US-PAT-NO: 5724566
DOCUMENT-IDENTIFIER: US 5724566 A

TITLE: Pipelined data processing including interrupts

DATE-ISSUED: March 3, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Swoboda; Gary L.	Sugar Land	TX		
Hammes; Mark R.	Houston	TX		
Deao; Douglas	Brookshire	TX		
Balmer; Keith	Bedford			GB2
Ing-Simmons; Nick	Huntingdon			GB2

US-CL-CURRENT: 712/245; 712/205

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 5. Document ID: US 5594864 A

L6: Entry 5 of 10   File: USPT   Jan 14, 1997

US-PAT-NO: 5594864
DOCUMENT-IDENTIFIER: US 5594864 A
** See image for **Certificate of Correction** **

TITLE: Method and apparatus for unobtrusively monitoring processor states and
characterizing bottlenecks in a pipelined processor executing grouped instructions

DATE-ISSUED: January 14, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Trauben; Richard D.	Morgan Hill	CA		

US-CL-CURRENT: <u>714/39</u>; <u>712/227</u>, <u>714/30</u>, <u>714/31</u>, <u>714/38</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 6.   Document ID: US 5574937 A

L6: Entry 6 of 10   File: USPT   Nov 12, 1996

US-PAT-NO: 5574937
DOCUMENT-IDENTIFIER: US 5574937 A
** See image for **Certificate of Correction** **

TITLE: Method and apparatus for improving instruction tracing operations in a
computer system

DATE-ISSUED: November 12, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Narain; Bikas	Gilbert	AZ		

US-CL-CURRENT: <u>712/23</u>; <u>712/200</u>, <u>714/45</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 7.   Document ID: US 5564028 A

L6: Entry 7 of 10   File: USPT   Oct 8, 1996

US-PAT-NO: 5564028
DOCUMENT-IDENTIFIER: US 5564028 A

TITLE: Pipelined data processing including instruction trace

DATE-ISSUED: October 8, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY

Swoboda; Gary L.   Sugar Land   TX
Hammes; Mark .R.   Houston   TX
Deao; Douglas   Brookshire   TX

US-CL-CURRENT: <u>712</u>/<u>227</u>; <u>710</u>/<u>264</u>, <u>714</u>/<u>45</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 8. Document ID: US 5438670 A

L6: Entry 8 of 10   File: USPT   Aug 1, 1995

US-PAT-NO: 5438670
DOCUMENT-IDENTIFIER: US 5438670 A
** See image for <u>Certificate of Correction</u> **

TITLE: Method of prechecking the validity of a write access request

DATE-ISSUED: August 1, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Baror; Gigi	Austin	TX		
Beck; Moti	Tel Aviv			IL
Biran; Dan	Tel Aviv			IL
Cohen; Elliot	Tel Aviv			IL
Hadas; Yair	Tel Aviv			IL
Konstantin; Benny	Kfar Saba			IL
Levy; Jonanthan	Kfar Saba			IL
Marko; Reuven	Netanya			IL
Ostrer; Aharon	Tel Aviv			IL
Saban; Rami	Holon			IL
Shackam; Alon	Tel Aviv			IL
Shahar; Boaz	Nof Yam			IL

US-CL-CURRENT: <u>711</u>/<u>3</u>; <u>711</u>/<u>169</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 9. Document ID: US 4945479 A

L6: Entry 9 of 10   File: USPT   Jul 31, 1990

US-PAT-NO: 4945479
DOCUMENT-IDENTIFIER: US 4945479 A

TITLE: Tightly coupled scientific processing system

DATE-ISSUED: July 31, 1990

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rusterholz; John T.	Roseville	MN		
Homan; Charles J.	St. Paul	MN		
Brown; Lowell E.	Anoka	MN		
Bennett; Donald B.	Burnsville	MN		
Malnati; Robert J.	St. Paul	MN		
Hamstra; James R.	Plymouth	MN		

US-CL-CURRENT: 712/3

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

◻ 10.   Document ID: US 4873630 A

L6: Entry 10 of 10   File: USPT   Oct 10, 1989

US-PAT-NO: 4873630
DOCUMENT-IDENTIFIER: US 4873630 A

TITLE: Scientific processor to support a host processor referencing common memory

DATE-ISSUED: October 10, 1989

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rusterholz; John T.	Roseville	MN		
Lahti; Archie E.	Fridley	MN		
Bushard; Louis B.	Anoka	MN		
Byers; Larry L.	Apple Valley	MN		
Hamstra; James R.	Plymouth	MN		
Homan; Charles J.	St. Paul	MN		

US-CL-CURRENT: 712/3; 712/5, 712/6, 712/9

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Term	Documents
TRACING	34057
TRACINGS	3039
INSTRUCTION	176048
INSTRUCTIONS	318095
INSTRUCTION?	0
INSTRUCTIONA	2

INSTRUCTIONB	1
INSTRUCTIONG	5
INSTRUCTIONI	1
INSTRUCTIONJ	1
INSTRUCTIONL	2
((TRACING NEAR5 INSTRUCTION?) AND INSTRUCTION PIPELINE?).PGPB,USPT.	10

There are more results than shown above. Click here to view the entire set.

Display Format: |- | Change Format

Previous Page   Next Page   Go to Doc#

Hit List

Search Results - Record(s) 1 through 10 of 10 returned.

☐ 1.   Document ID: US 20050055675 A1

L11: Entry 1 of 10   File: PGPB   Mar 10, 2005

PGPUB-DOCUMENT-NUMBER: 20050055675
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050055675 A1

TITLE: Generation of software objects from a hardware description

PUBLICATION-DATE: March 10, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Neifert, William	Arlington	MA	US	
Marantz, Joshua	Brookline	MA	US	
Sayde, Richard	Carlisle	MA	US	
Tatham, Joseph	Arlington	MA	US	
Lehotsky, Alan	Carlisle	MA	US	
Ladd, Andrew	Maynard	MA	US	
Seneski, Mark	Roslindale	MA	US	
Atkins, Aron	Arlington	MA	US	

US-CL-CURRENT: 717/135; 703/22, 717/138, 717/141, 717/154

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 2.   Document ID: US 20040122644 A1

L11: Entry 2 of 10   File: PGPB   Jun 24, 2004

PGPUB-DOCUMENT-NUMBER: 20040122644
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040122644 A1

TITLE: Optimized execution of software objects generated from a hardware
description

PUBLICATION-DATE: June 24, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47

Neifert, William	Arlington	MA	US
Marantz, Joshua	Brookline	MA	US
Sayde, Richard	Carlisle	MA	US
Tatham, Joseph	Arlington	MA	US
Lehotsky, Alan	Carlisle	MA	US
Ladd, Andrew	Maynard	MA	US
Seneski, Mark	Roslindale	MA	US
Atkins, Aron	Arlington	MA	US

US-CL-CURRENT: 703/16

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 3.   Document ID: US 20040117168 A1

L11: Entry 3 of 10   File: PGPB   Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117168
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117168 A1

TITLE: Global analysis of software objects generated from a hardware description

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Neifert, William	Arlington	MA	US	
Marantz, Joshua	Brookline	MA	US	
Sayde, Richard	Carlisle	MA	US	
Tatham, Joseph	Arlington	MA	US	
Lehotsky, Alan	Carlisle	MA	US	
Ladd, Andrew	Maynard	MA	US	
Seneski, Mark	Roslindale	MA	US	
Atkins, Aron	Arlington	MA	US	

US-CL-CURRENT: 703/14

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 4.   Document ID: US 20040117167 A1

L11: Entry 4 of 10   File: PGPB   Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117167
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117167 A1

TITLE: Simulation of software objects generated from a hardware description

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Neifert, William	Arlington	MA	US	
Marantz, Joshua	Brookline	MA	US	
Sayde, Richard	Carlisle	MA	US	
Tatham, Joseph	Arlington	MA	US	
Lehotsky, Alan	Carlisle	MA	US	
Ladd, Andrew	Maynard	MA	US	
Seneski, Mark	Roslindale	MA	US	
Atkins, Aron	Arlington	MA	US	

US-CL-CURRENT: 703/14

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 5. Document ID: US 20030139917 A1

L11: Entry 5 of 10   File: PGPB   Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030139917
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030139917 A1

TITLE: Late binding of resource allocation in a performance simulation
infrastructure

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hardwick, Jonathan Christopher	Cambridge		GB	
Papaefstahiou, Efstathios	Cambridge		GB	

US-CL-CURRENT: 703/21

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 6. Document ID: US 20030121027 A1

L11: Entry 6 of 10   File: PGPB   Jun 26, 2003

PGPUB-DOCUMENT-NUMBER: 20030121027
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030121027 A1

TITLE: Behavioral abstractions for debugging coordination-centric software designs

PUBLICATION-DATE: June 26, 2003

INVENTOR-INFORMATION:
NAME   CITY   STATE   COUNTRY   RULE-47
Hines, Kenneth J.   Bothell   WA   US

US-CL-CURRENT: <u>717</u>/<u>135</u>; <u>717</u>/<u>113</u>, <u>717</u>/<u>120</u>, <u>719</u>/<u>318</u>, <u>719</u>/<u>321</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 7. Document ID: US 20030028858 A1

L11: Entry 7 of 10   File: PGPB   Feb 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030028858
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030028858 A1

TITLE: Evolution diagrams for debugging distributed embedded software applications

PUBLICATION-DATE: February 6, 2003

INVENTOR-INFORMATION:
NAME   CITY   STATE   COUNTRY   RULE-47
Hines, Kenneth J.   Bothell   WA   US

US-CL-CURRENT: <u>717</u>/<u>125</u>; <u>717</u>/<u>130</u>, <u>717</u>/<u>131</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 8. Document ID: US 20020174415 A1

L11: Entry 8 of 10   File: PGPB   Nov 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020174415
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020174415 A1

TITLE: System and method for debugging distributed software environments

PUBLICATION-DATE: November 21, 2002

INVENTOR-INFORMATION:
NAME   CITY   STATE   COUNTRY   RULE-47
Hines, Kenneth J.   Bothell   WA   US

US-CL-CURRENT: <u>717</u>/<u>127</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 9. Document ID: US 20020087953 A1

L11: Entry 9 of 10   File: PGPB   Jul 4, 2002

PGPUB-DOCUMENT-NUMBER: 20020087953
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020087953 A1

TITLE: Data structure and method for detecting constraint conflicts in
coordination-centric software systems

PUBLICATION-DATE: July 4, 2002

INVENTOR-INFORMATION:
NAME   CITY   STATE   COUNTRY   RULE-47
Hines, Kenneth J.   Bothell   WA   US

US-CL-CURRENT: 717/125

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D |

☐ 10.   Document ID: US 20020062463 A1

L11: Entry 10 of 10   File: PGPB   May 23, 2002

PGPUB-DOCUMENT-NUMBER: 20020062463
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020062463 A1

TITLE: Dynamic control graphs for analysis of coordination-centric software designs

PUBLICATION-DATE: May 23, 2002

INVENTOR-INFORMATION:
NAME   CITY   STATE   COUNTRY   RULE-47
Hines, Kenneth J.   Bothell   WA   US

US-CL-CURRENT: 714/38

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D |

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Term	Documents
TRACE	161110
TRACES	101018
INSTRUCTION?	0
INSTRUCTIONA	2
INSTRUCTIONB	1

INSTRUCTIONG	5
INSTRUCTIONI	1
INSTRUCTIONJ	1
INSTRUCTIONL	2
INSTRUCTIONN	12
INSTRUCTIONS	318095
(L8 AND TRACE AND INSTRUCTION?).PGPB,USPT.	10

There are more results than shown above. Click here to view the entire set.

Display Format: |-_____| [Change Format]

Previous Page   Next Page   Go to Doc#